IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Inventors:

Michael C. Dorsey

Serial No.:

09/976,490

Filing Date:

October 12, 2001

For: ASIC BIST Controller Employing

Multiple Clock Domains

Atty. Dkt. No.: 5681-55200

Examiner: Unknown

Art Unit: 2133

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date

indicated below:

B. Noel Kivlin Name of Registered Representative

SUBMISSION OF FORMAL DRAWINGS

Official Draftsman

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 RECEIVED

JUL 1.7 2003

Technology Center 2100

Sir:

Applicant hereby submits the formal drawings for the above-referenced application and requests that these drawings be accepted for filing.

Respectfully submitted,

B. Noël Kivlin

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